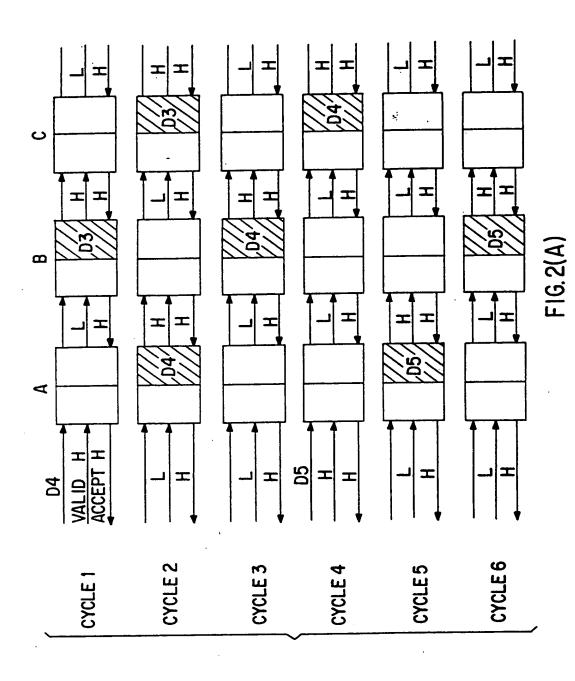
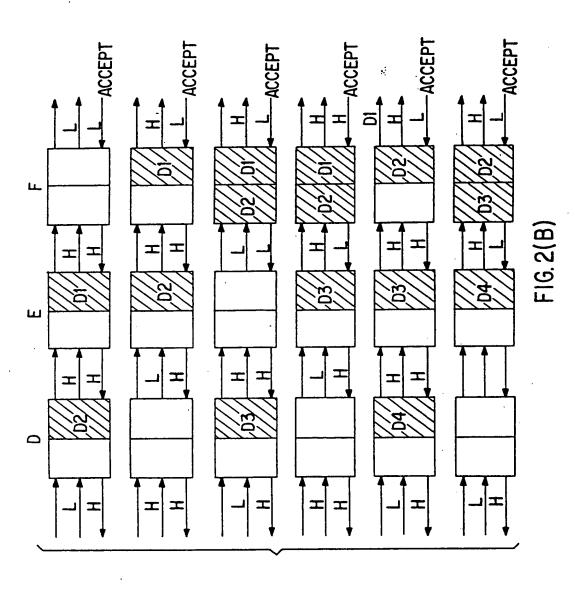
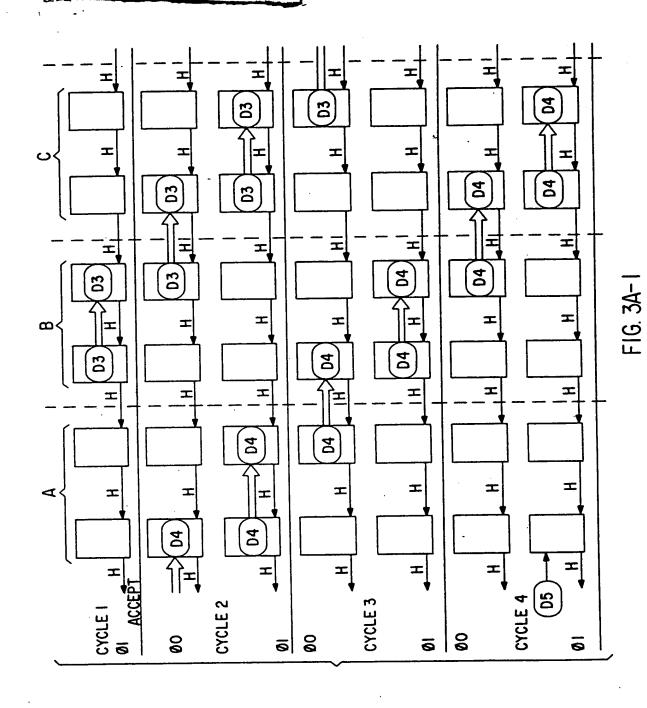


TOPICAL CARE







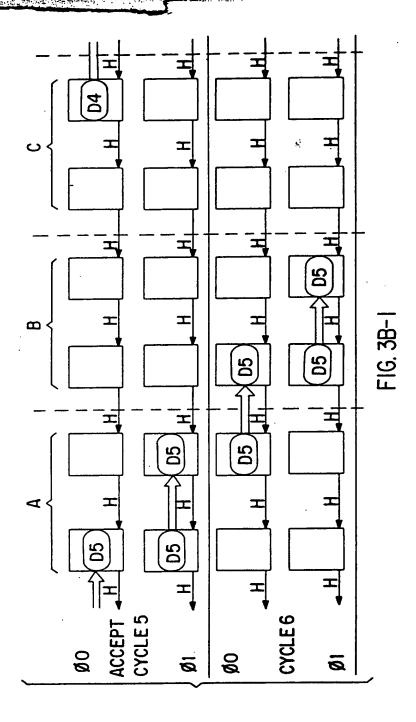
I

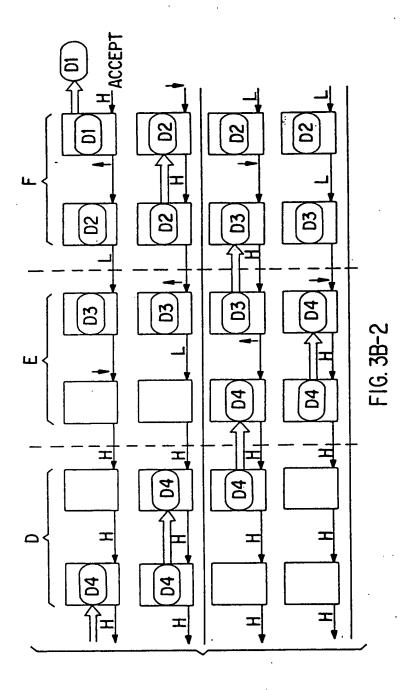
I

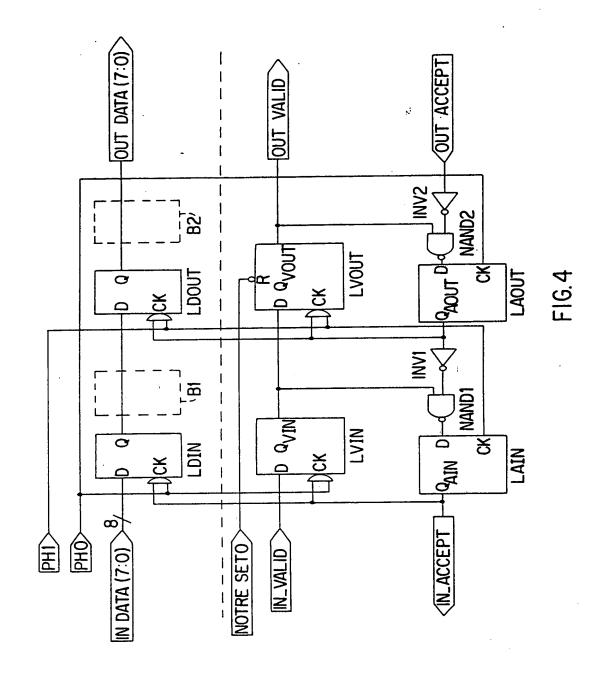
D3

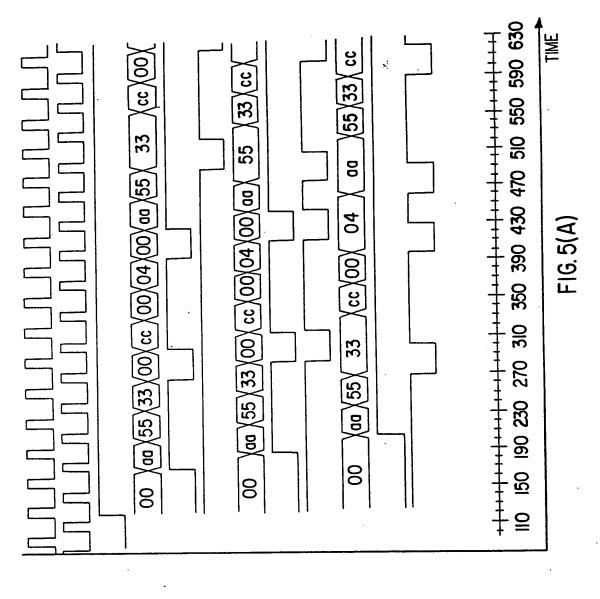
I

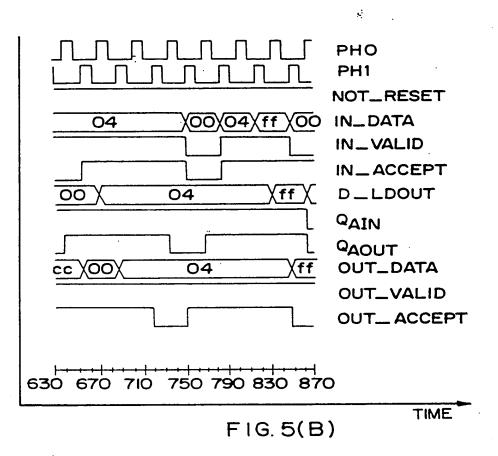
工











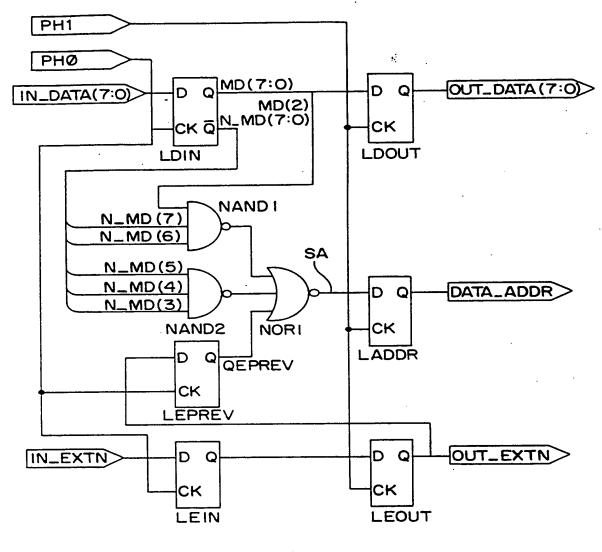


FIG. 6

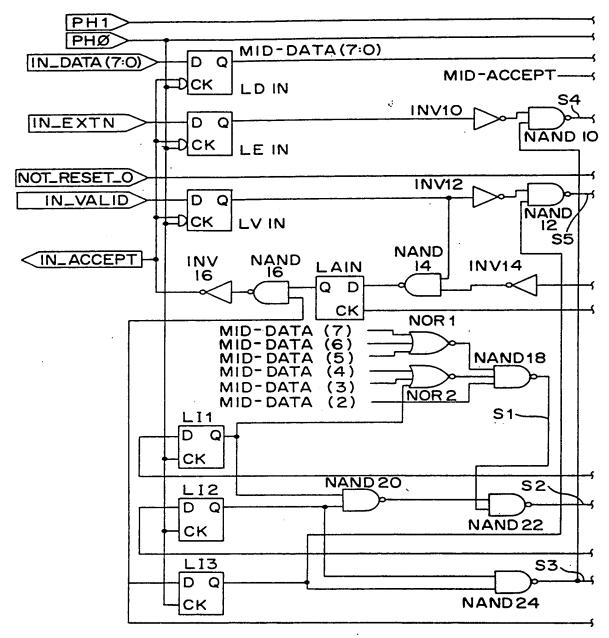


FIG. 8(A)

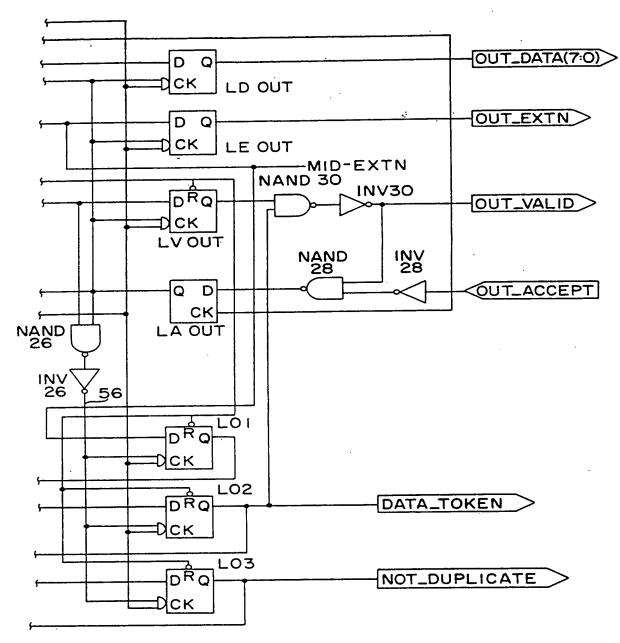


FIG. 8(B)

The state of the s

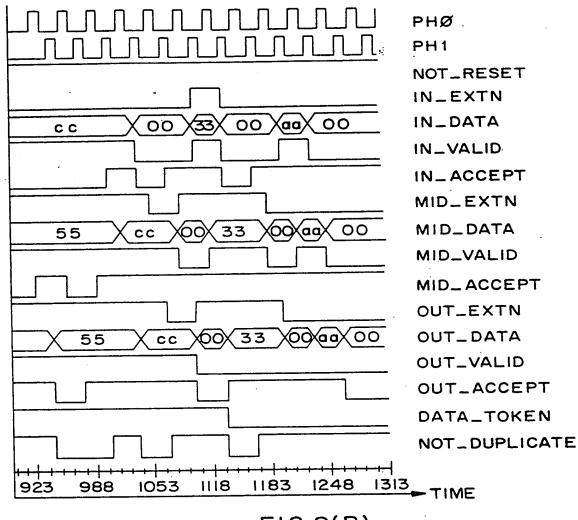


FIG. 9(B)

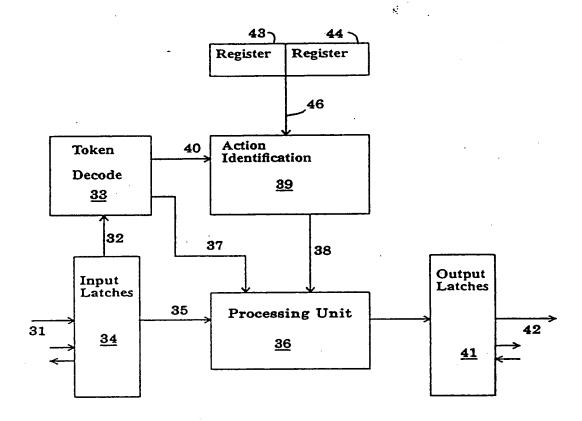
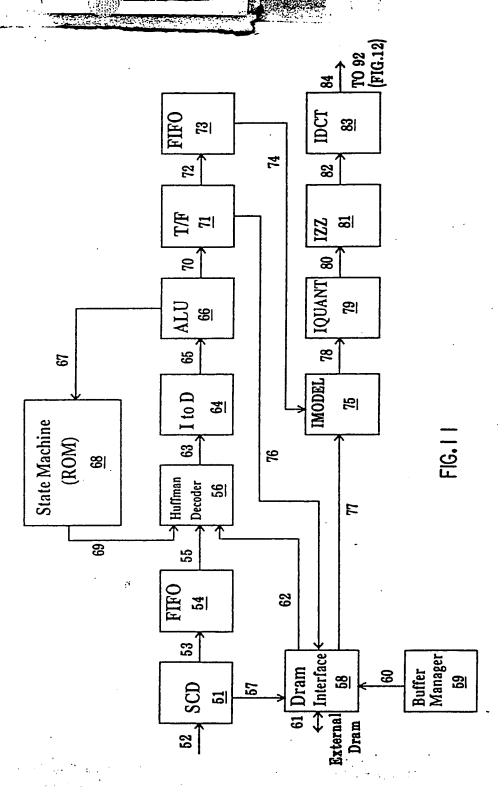


FIG. I O



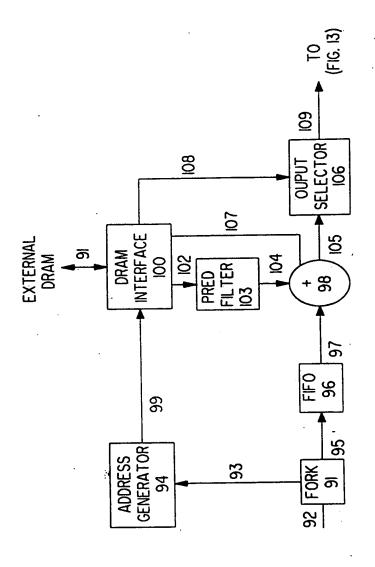
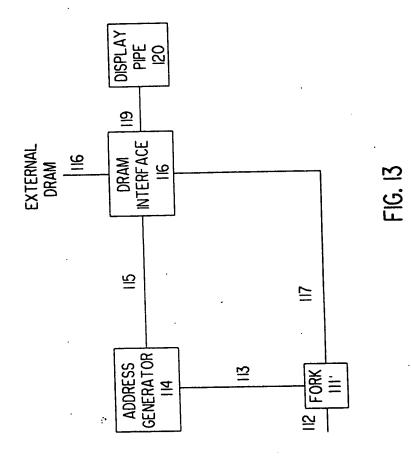
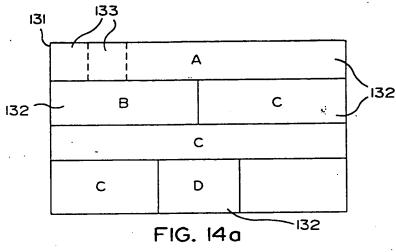


FIG. 12





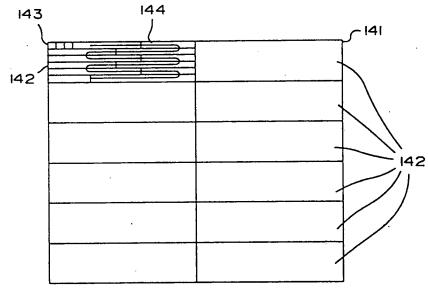
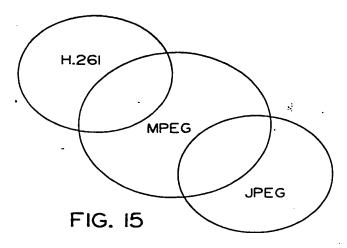


FIG. 14c

FIG. 14b



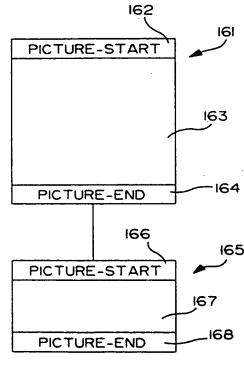
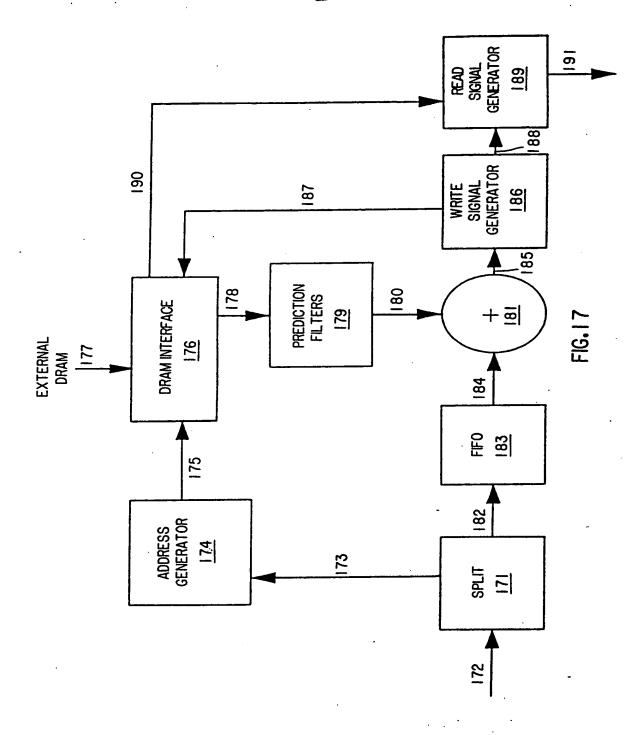
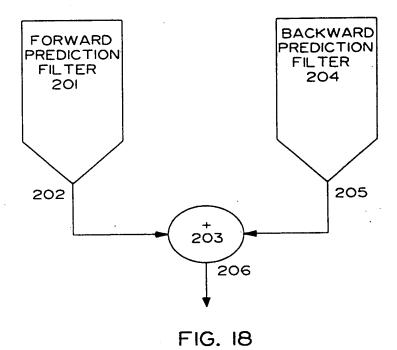
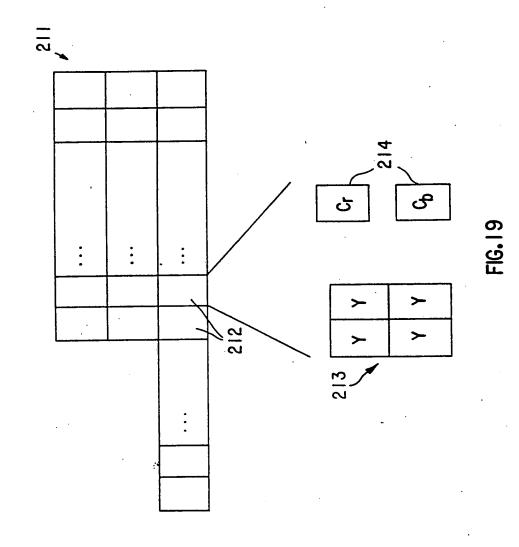
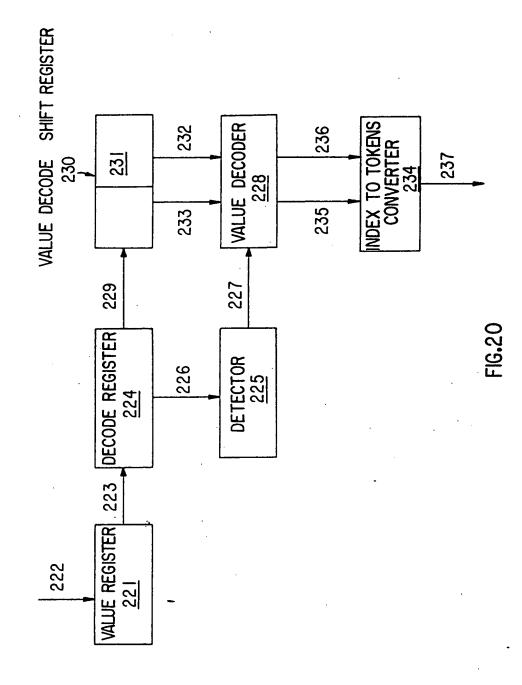


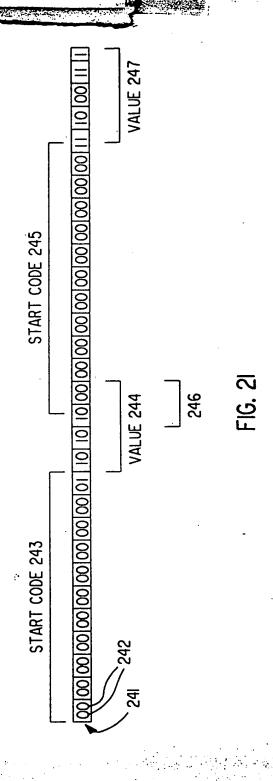
FIG. 16

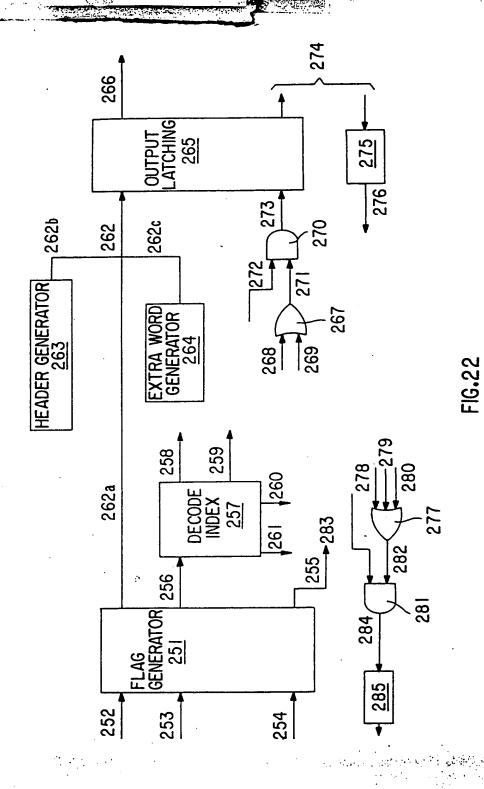












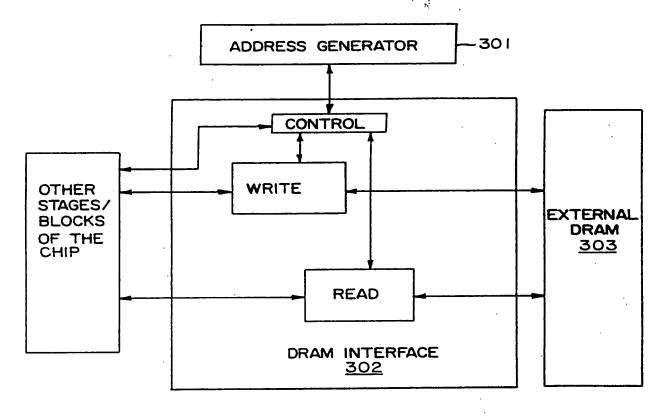


FIG.23

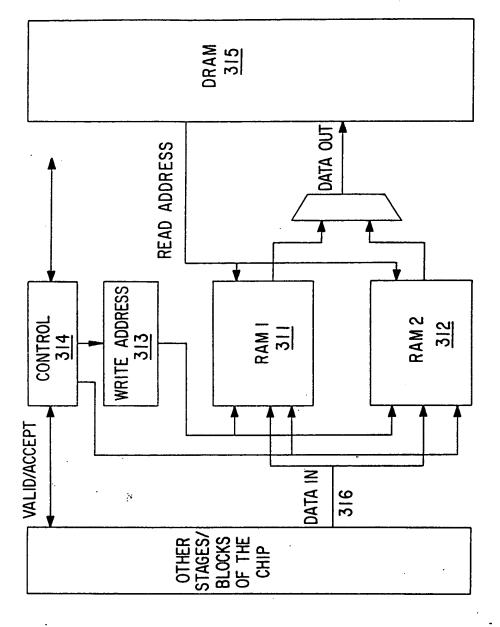


FIG.24

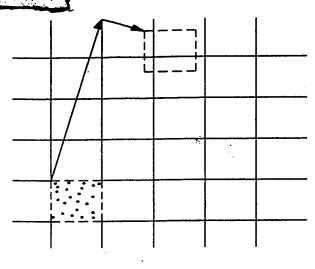


FIG. 25

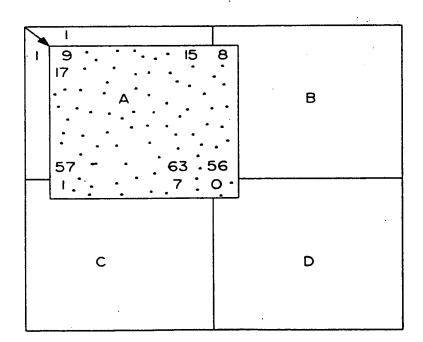
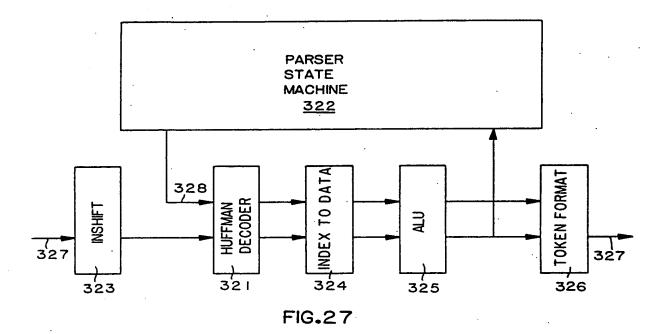
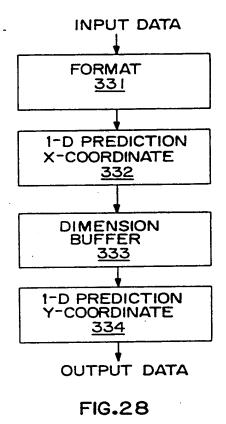


FIG. 26





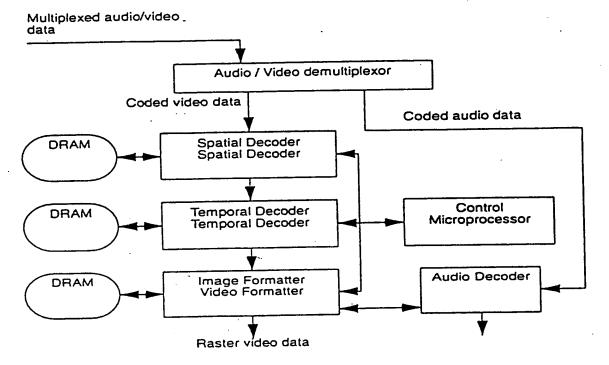
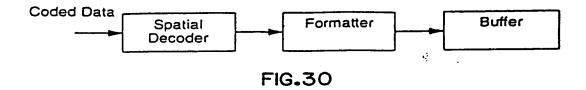
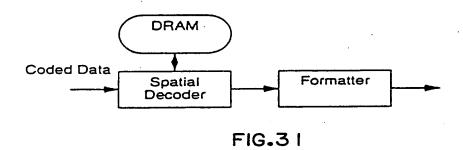


FIG.29





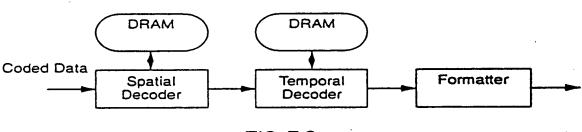
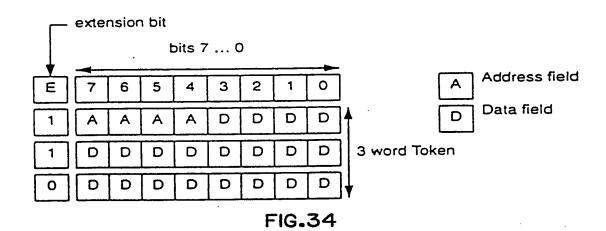
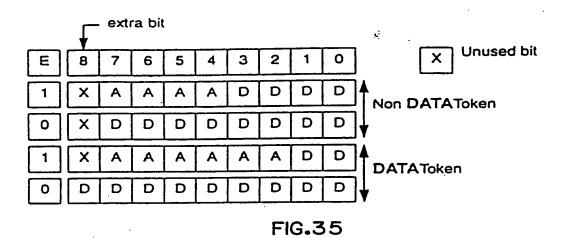


FIG.32









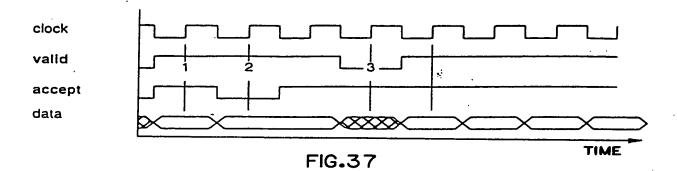
MPEG 4:2:0 macroblock

FIG.36A



JPEG 2:1:1 macroblock

FIG.36B



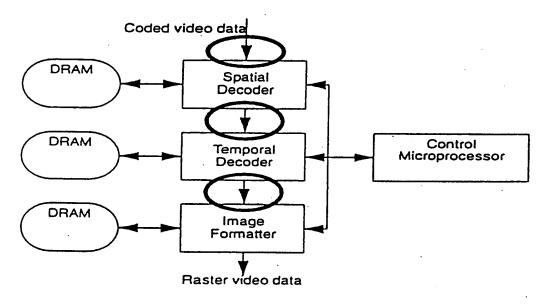


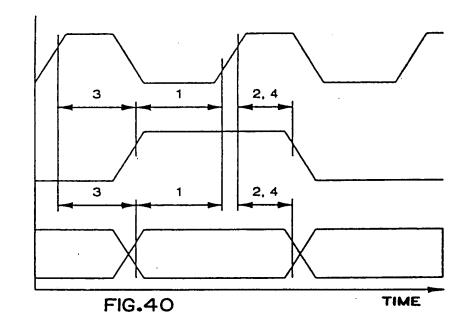
FIG.38



DOXYINED DIPORT

valid / accept

data / extn

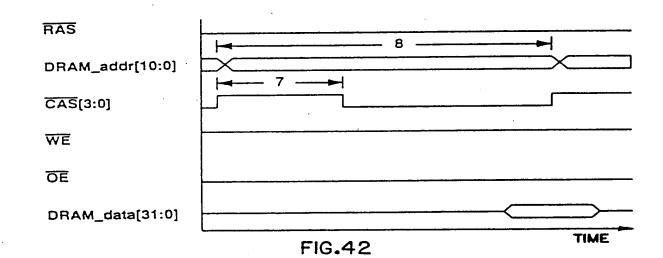


Access Start

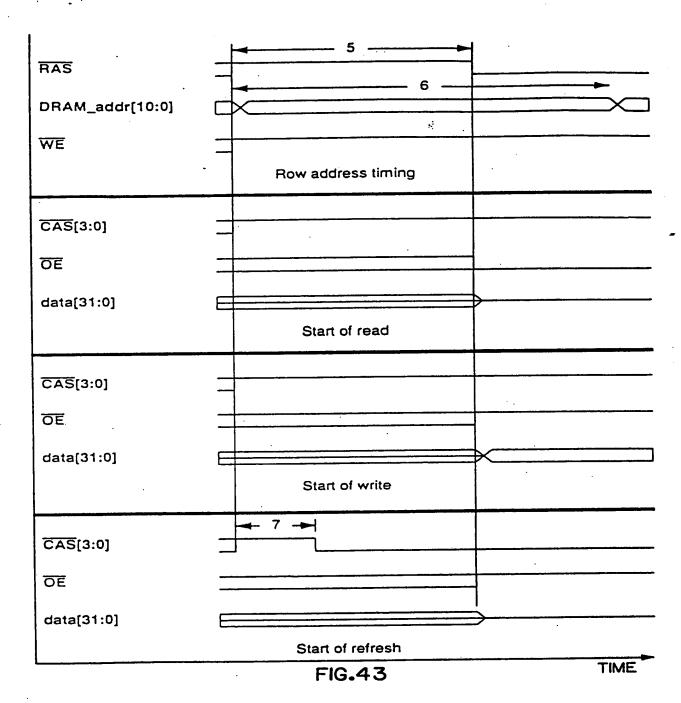
Data Transfer

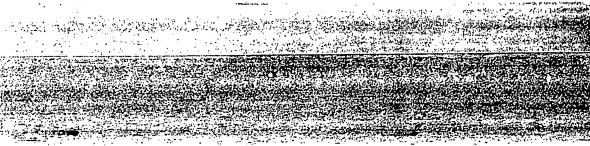
Default State

FIG.4 |









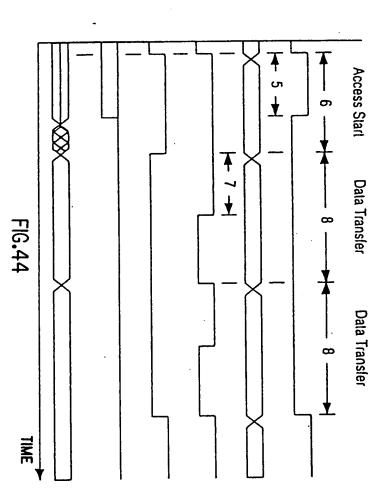
DRAM_data[31:0]

읾

CAS[3:0] M

DRAM_addr[10:0]

RAS



102710E3 C13901



DRAM_addr[10:0]

CAS[3:0]

WE

DRAM_data[31:0]

FIG.45

RAS

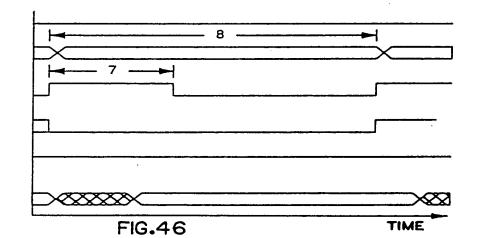
DRAM_addr[10:0]

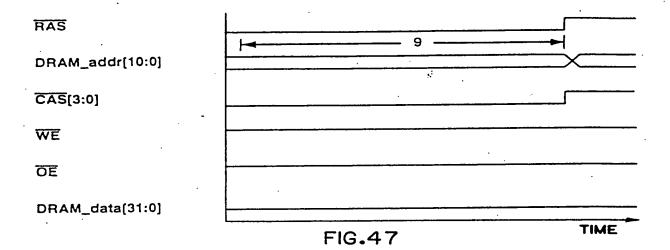
CAS[3:0]

WE

ŌĒ

DRAM_data[31:0]





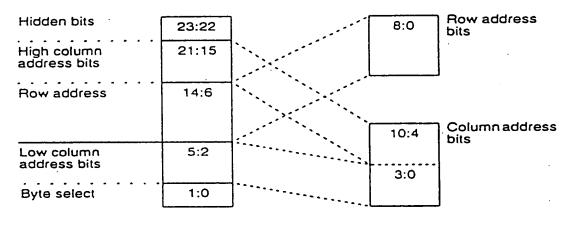
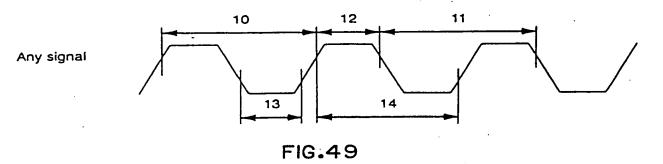
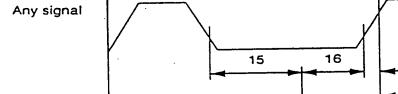
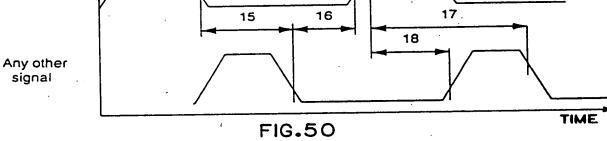
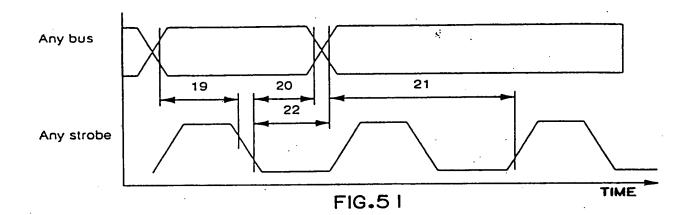


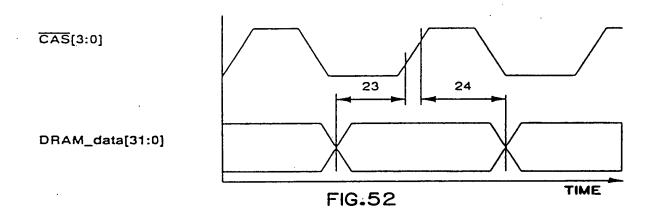
FIG.48







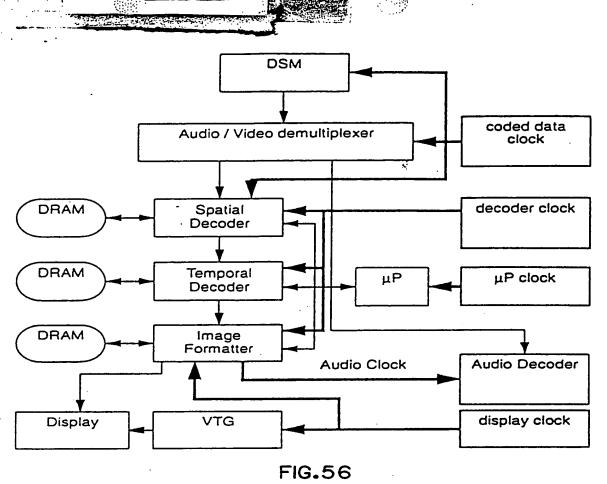


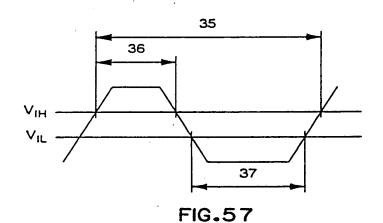


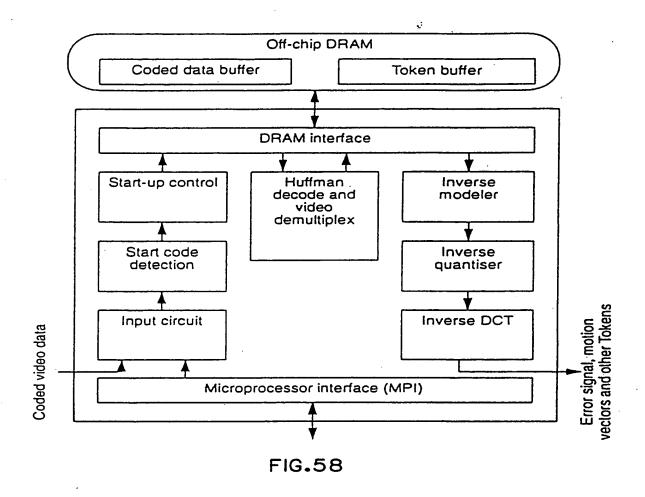
8 bit value 16 bit value 32 bit value bits[7:0] base + 3
bits[15:8] base + 2
bits[7:0] bits[23:16] base + 1
bits[7:0] bits[15:8] bits[31:24] base + 0

FIG.55

(7.1







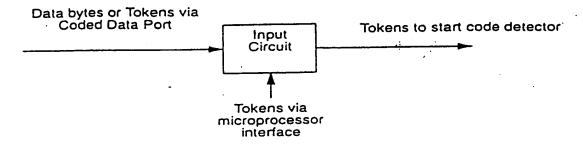
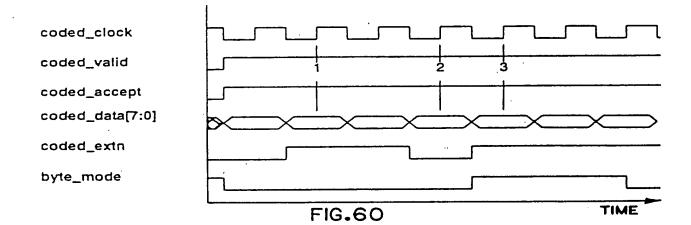
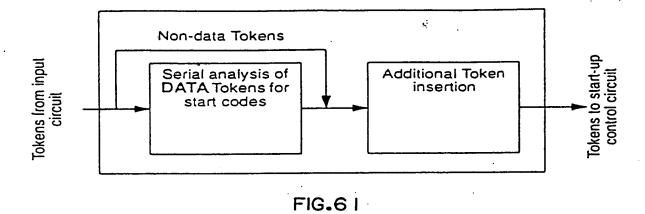


FIG.59





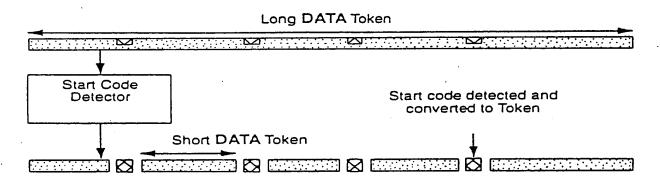
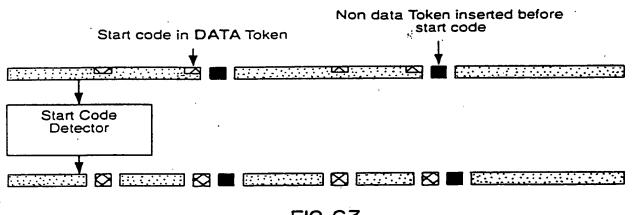


FIG.62



This looks like an MPEG picture start

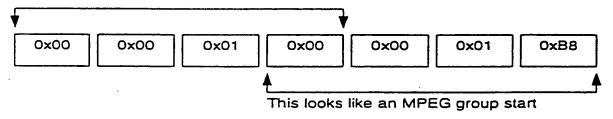


FIG.64

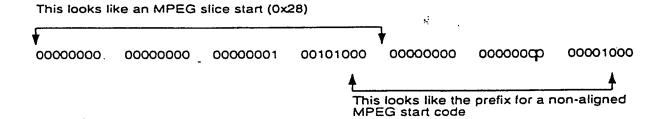


FIG.65

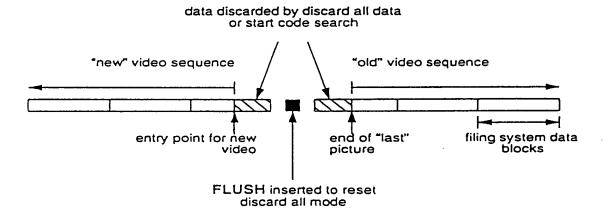
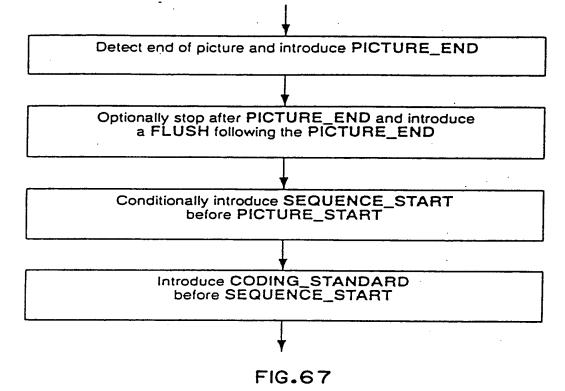
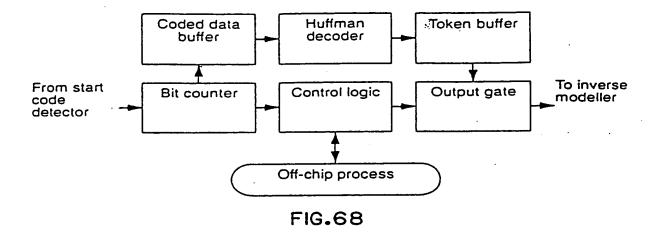
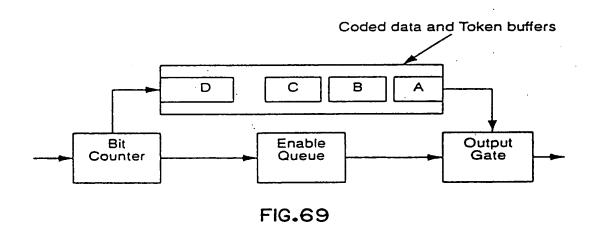
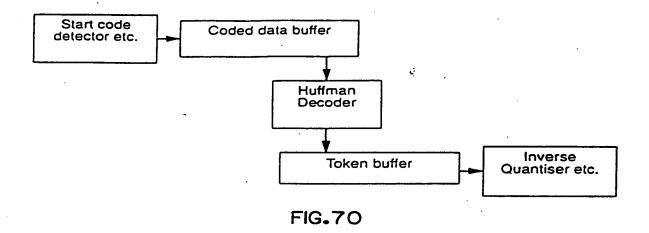


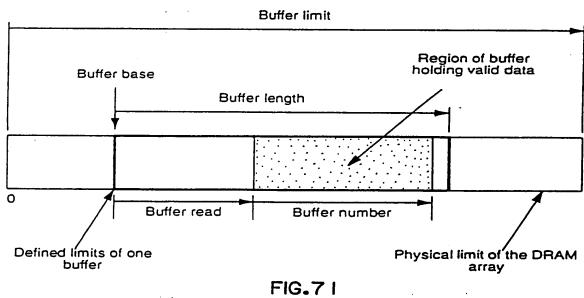
FIG.66

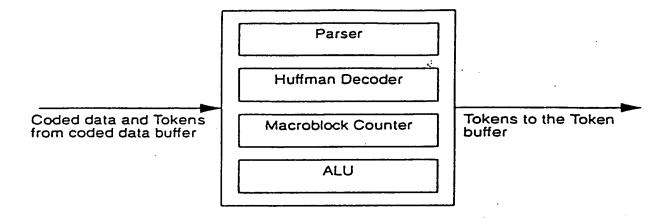


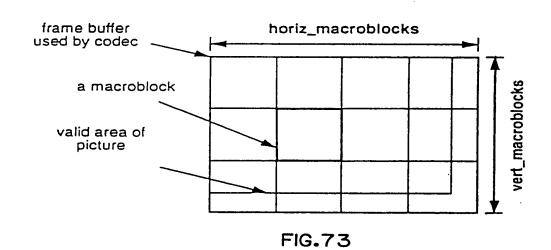


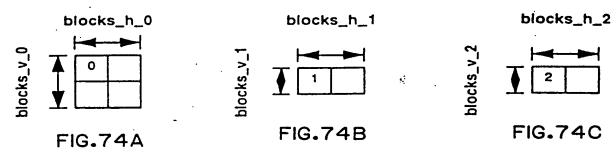


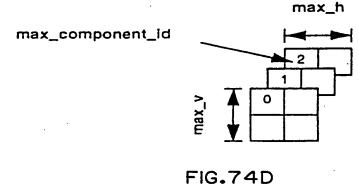












$$\begin{cases} horiz_macroblocks = \frac{horiz_pels + 15}{16} \\ vert_macroblocks = \frac{vert_pels + 15}{16} \end{cases}$$
FIG.75



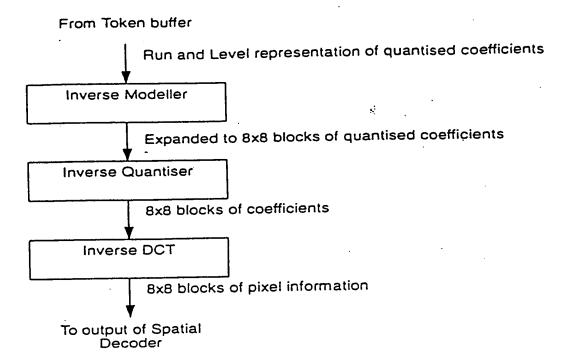
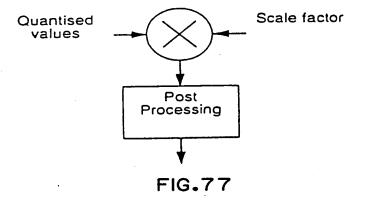
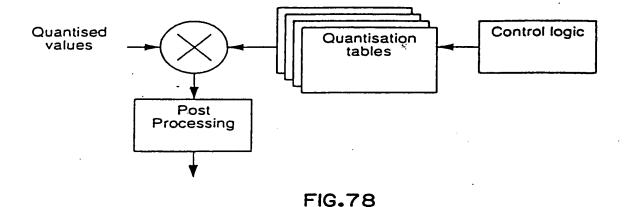


FIG.76





Scale factor

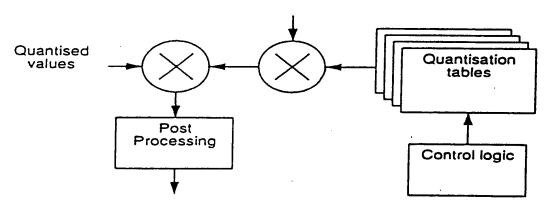


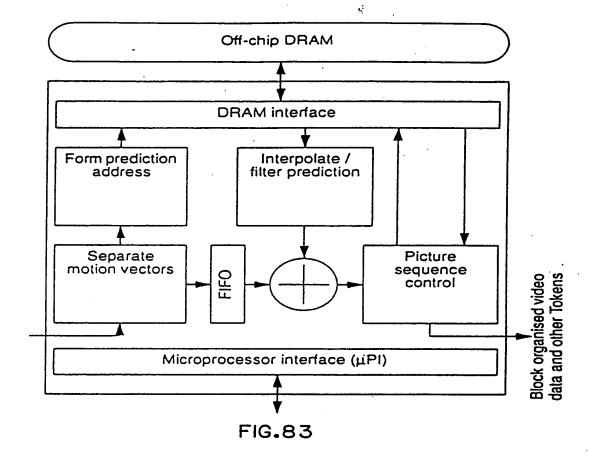
FIG.79

One scan

One frame

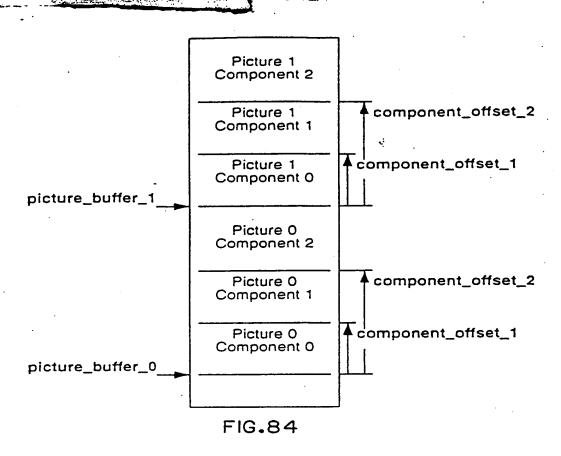
Scan

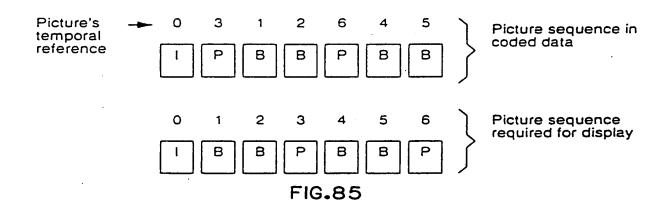
SEQUENCE_END

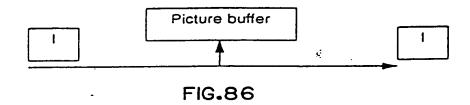


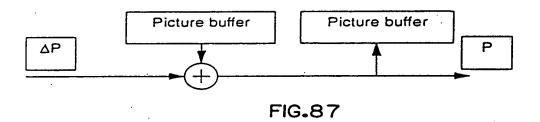
Error signal, motion vectors and other Tokens

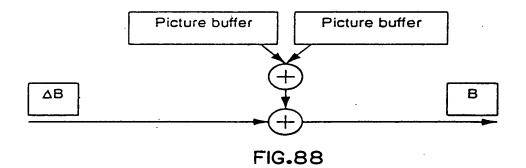
To the first first the first

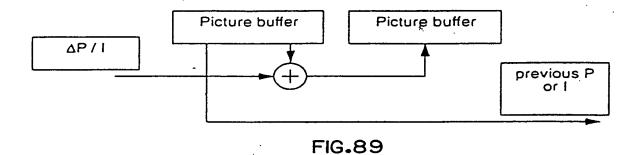


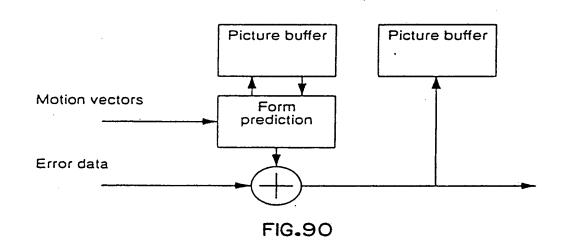


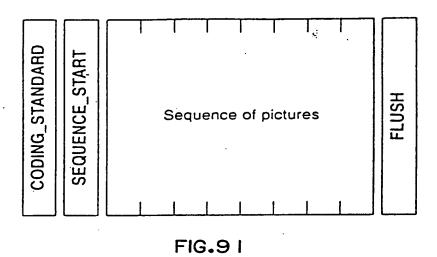


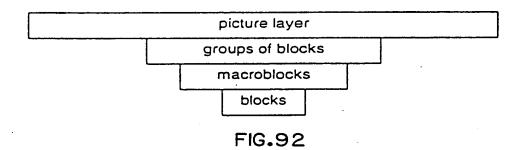


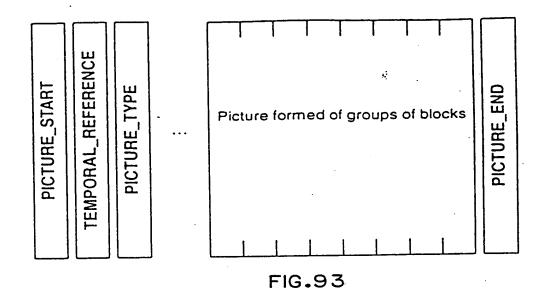


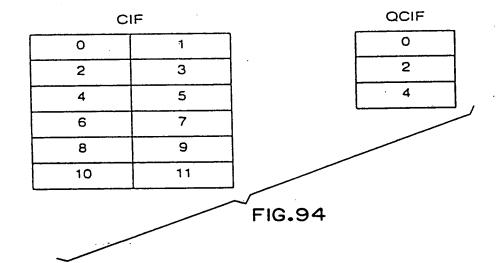


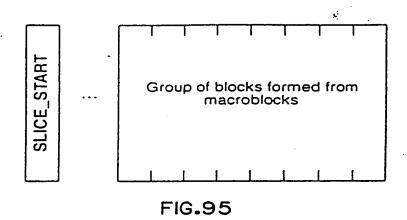






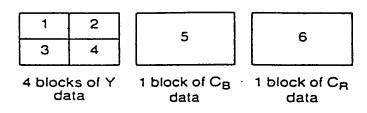






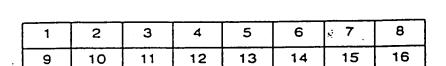
1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21.	22
23	24	25	26	27	28	29	30	31	32	33

FIG.96



DATA 00 DATA 00 DATA 00	DATA 00	DATA 02
-------------------------------	---------	---------

FIG.98



	59	58	59	60	61	62	63	64
1	4	1						

FIG.99

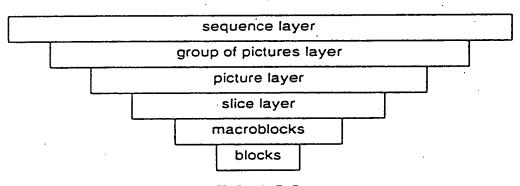
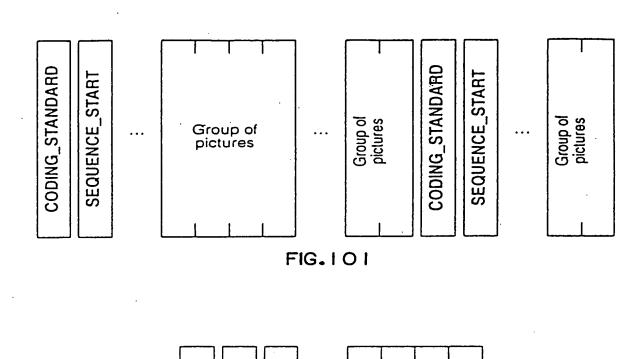


FIG. 100

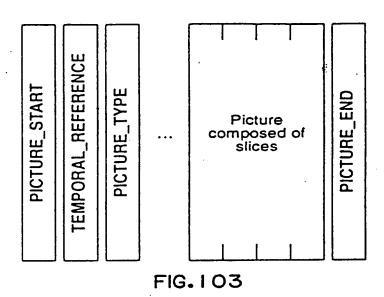


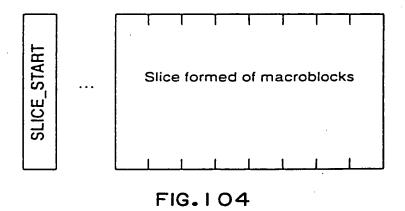
Group of pictures

BROKEN_CLOSED

TIME_CODE

GROUP_START





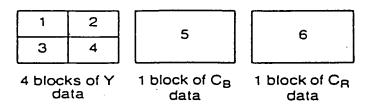


FIG. 1 05

DATA 00	4 4	DATA 00	DATA 01	DATA 02		DATA 00	DATA 00	DATA 00	DATA 00	DATA 01	DATA 02	••
---------	-----	---------	---------	---------	--	---------	---------	---------	---------	---------	---------	----

FIG. 106

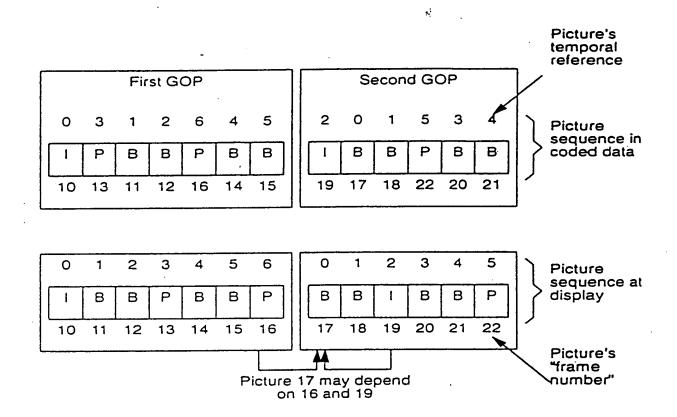


FIG. 107

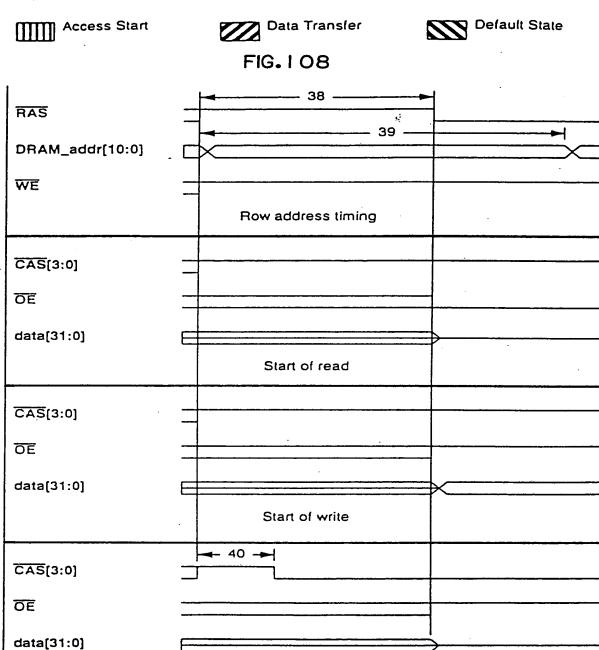
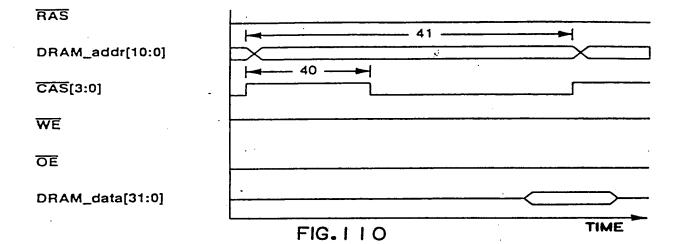
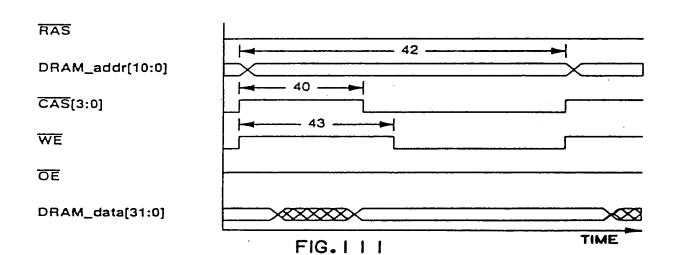
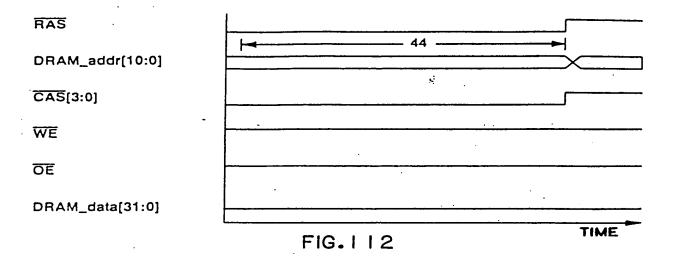


FIG. 109

Start of refresh







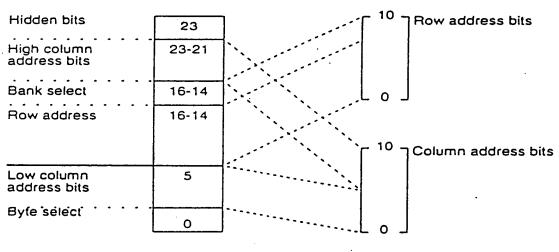
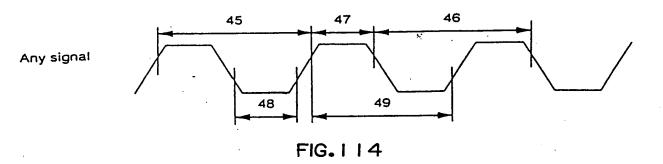
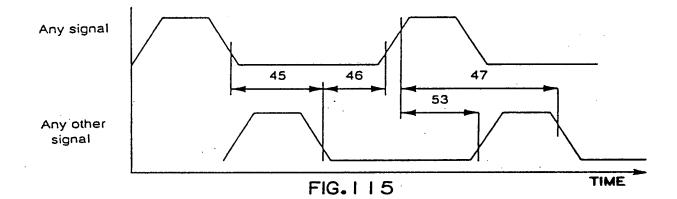
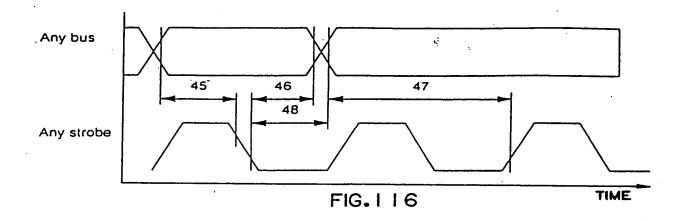
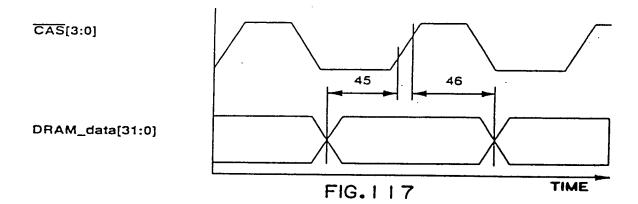


FIG. 1 13









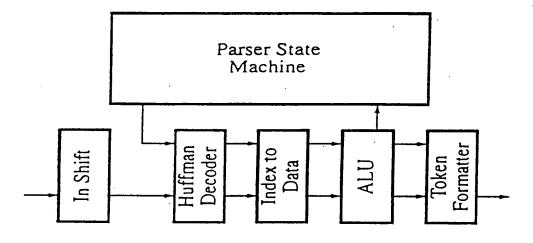
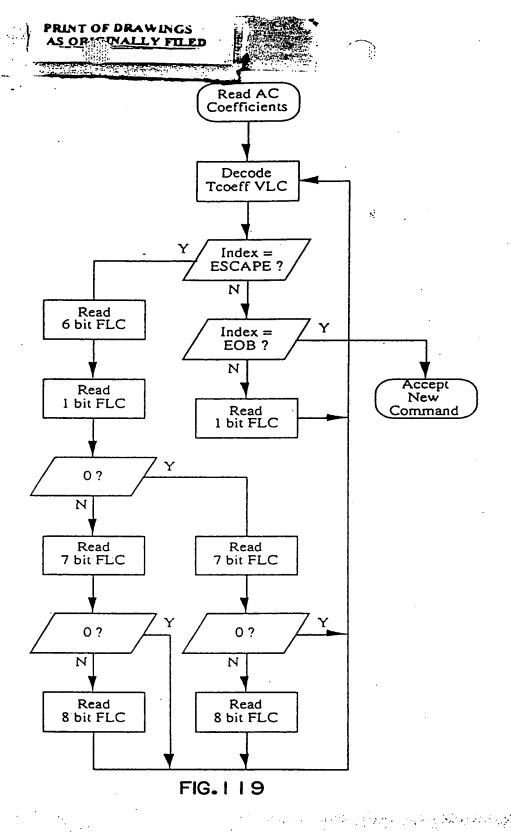
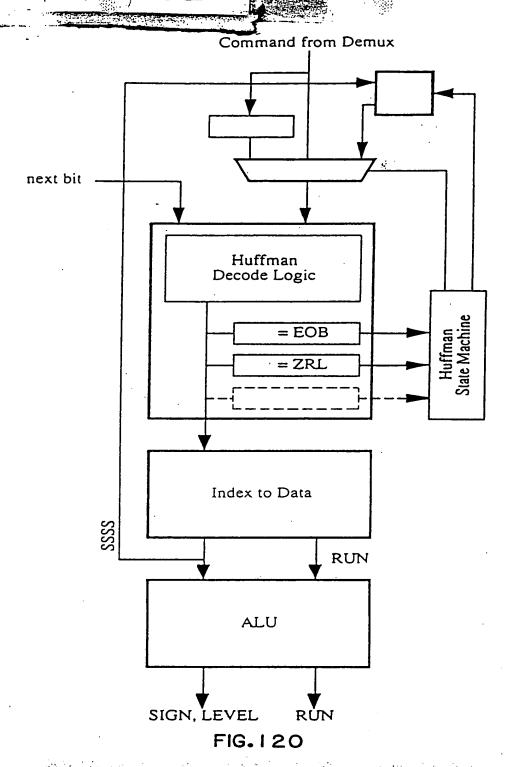


FIG. 1 18





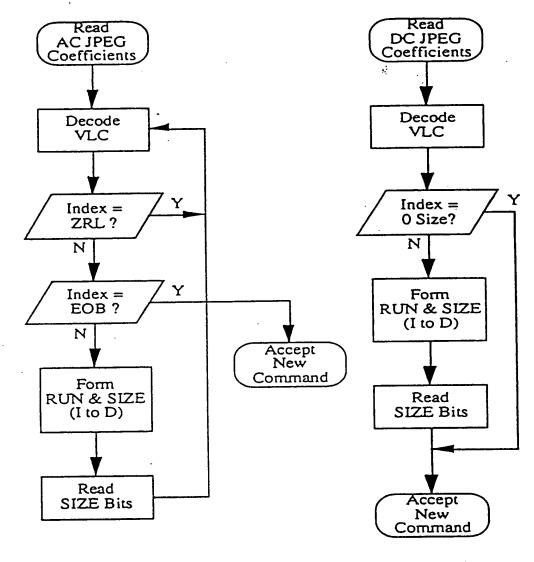


FIG. 121A

FIG. 121B

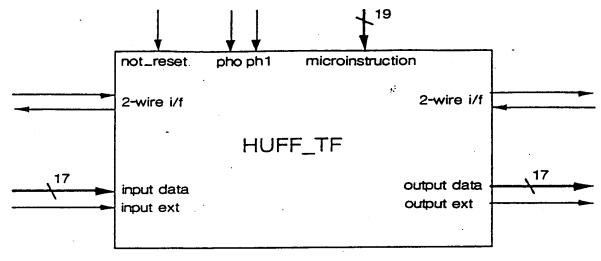


FIG. 122

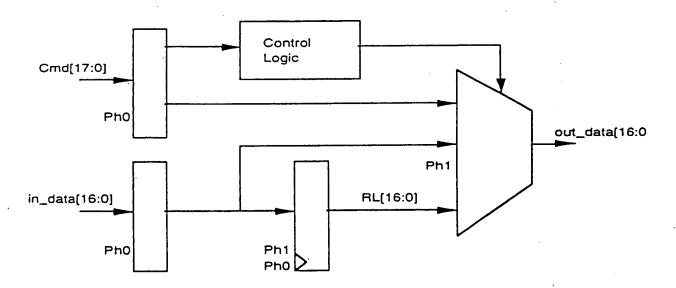
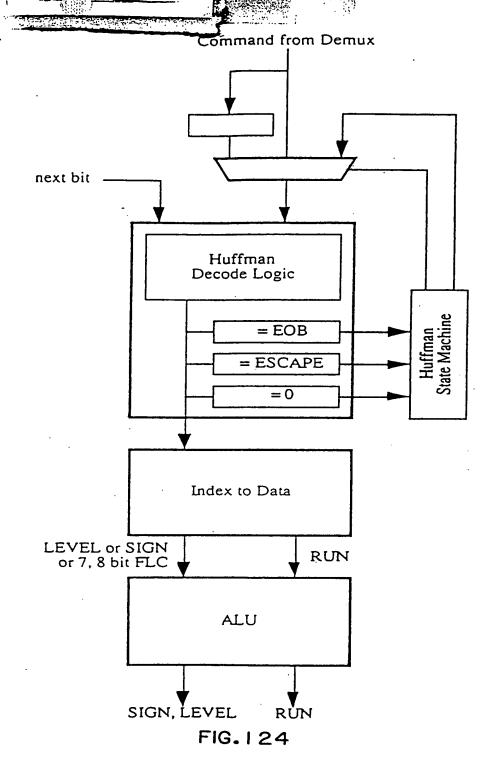
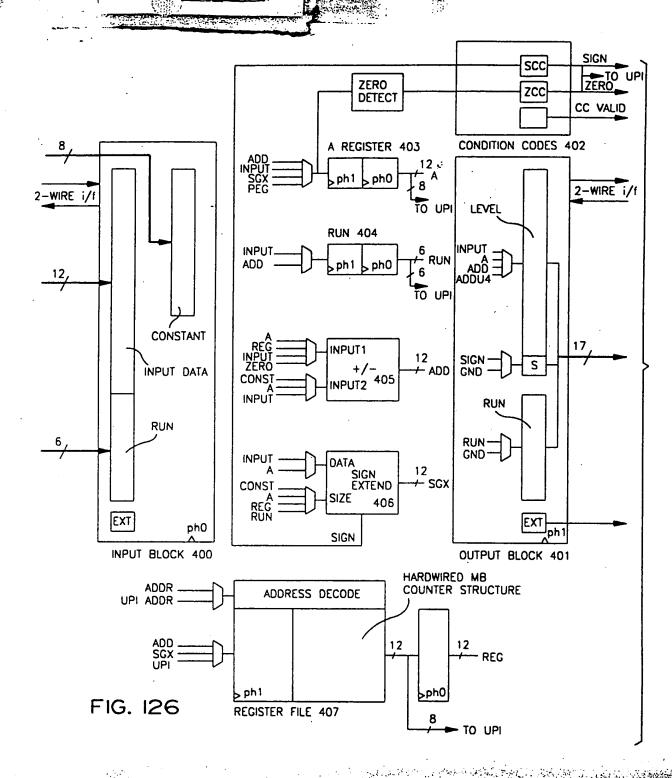


FIG. 123



The second secon



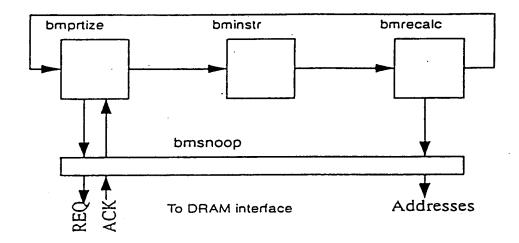


FIG. 127

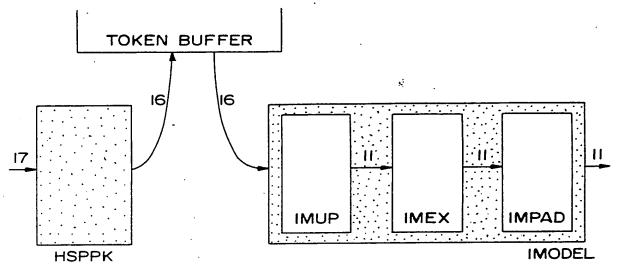


FIG. 128

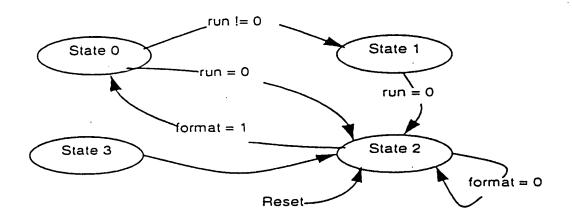


FIG. 129

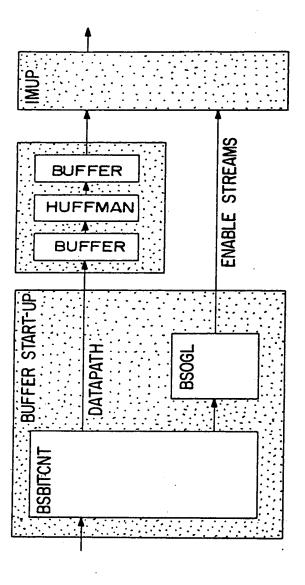


FIG. 130

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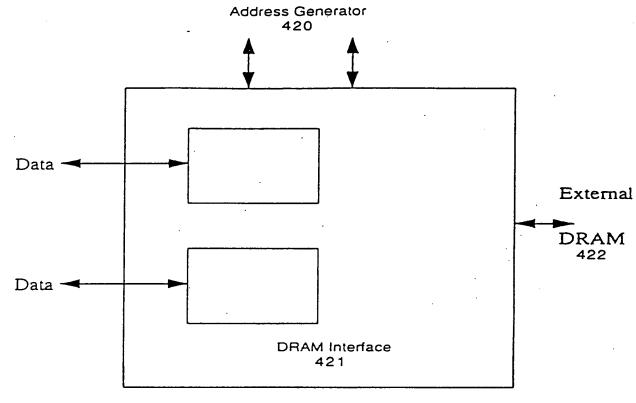


FIG. 131

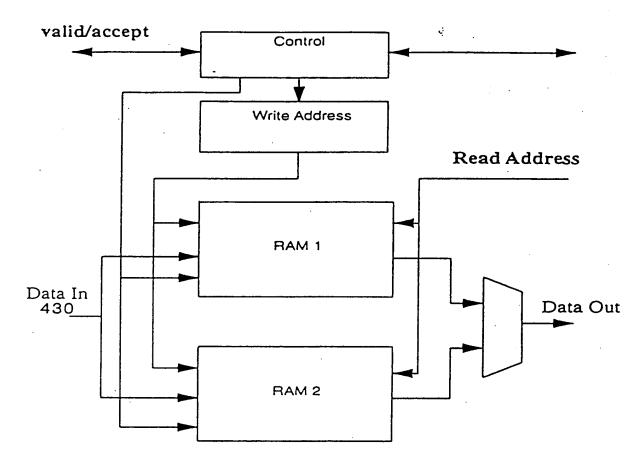
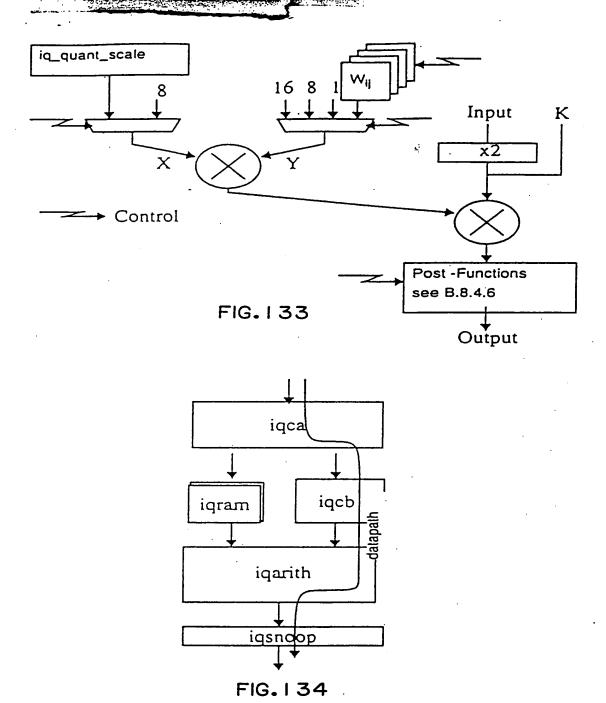


FIG. 132



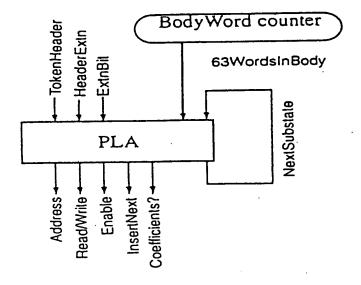


FIG. 135

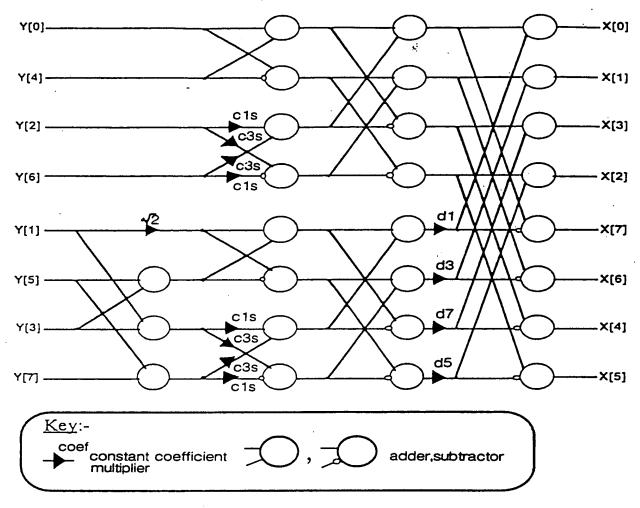


FIG. 136

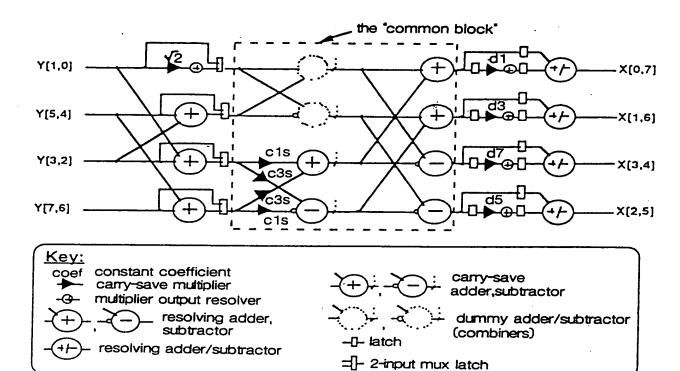


FIG. 137

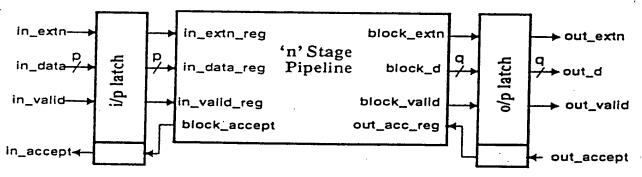


FIG. 138

DOFFINE BIPOLI

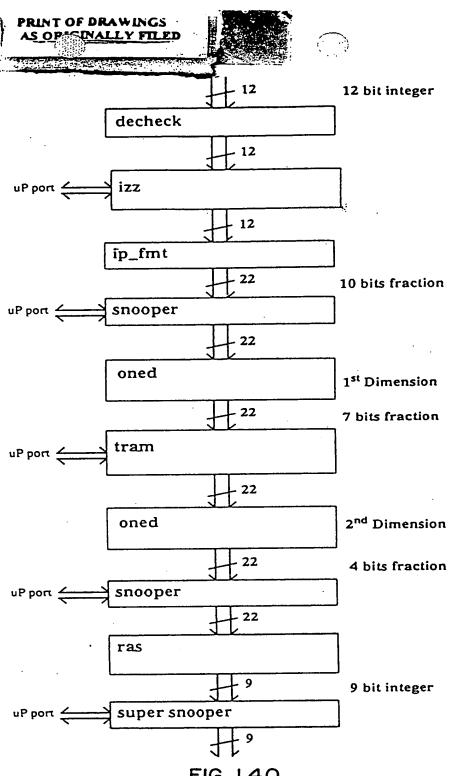
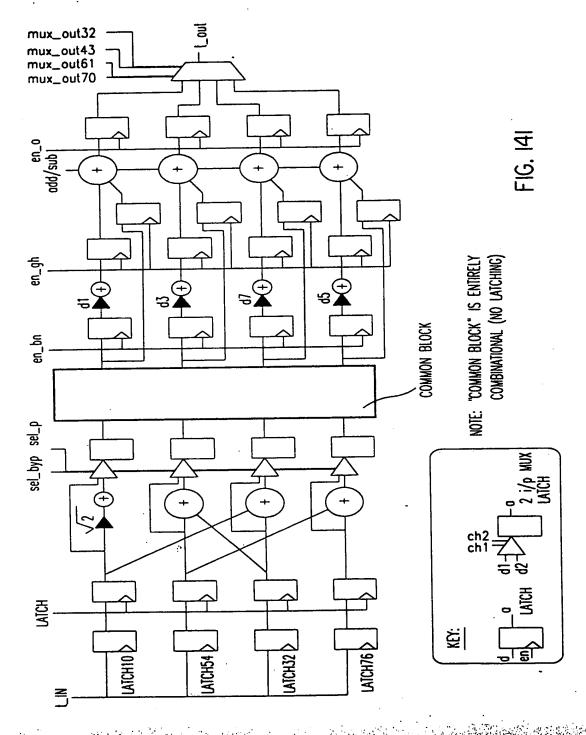


FIG. 140



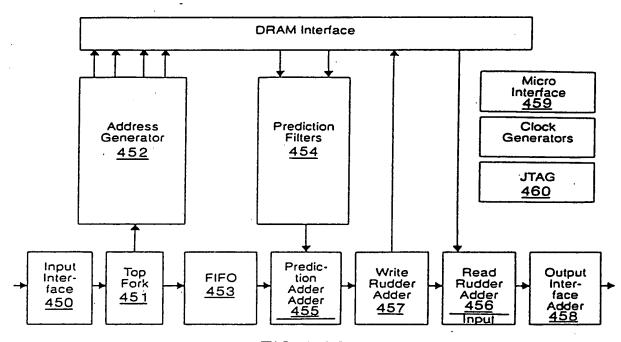


FIG. 142

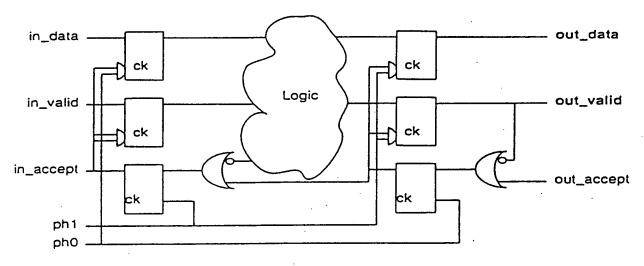


FIG. 143

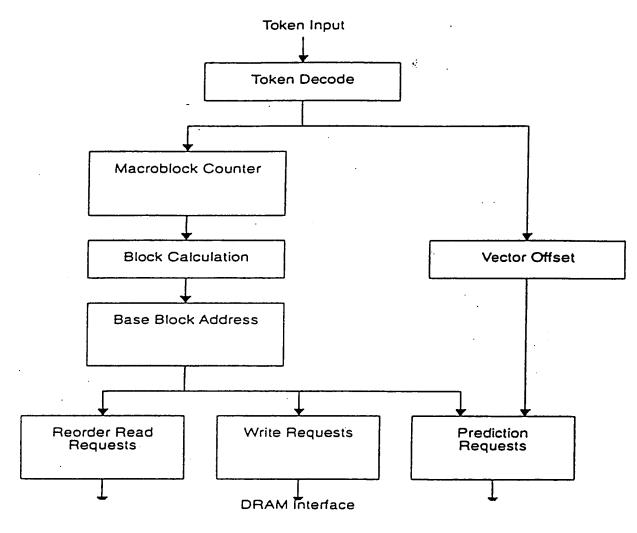


FIG. 144

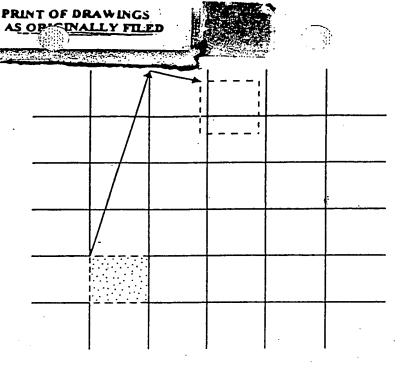


FIG. 145

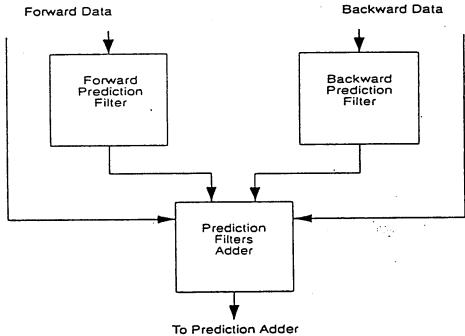


FIG. 146

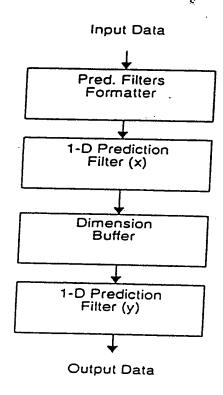


FIG. 147

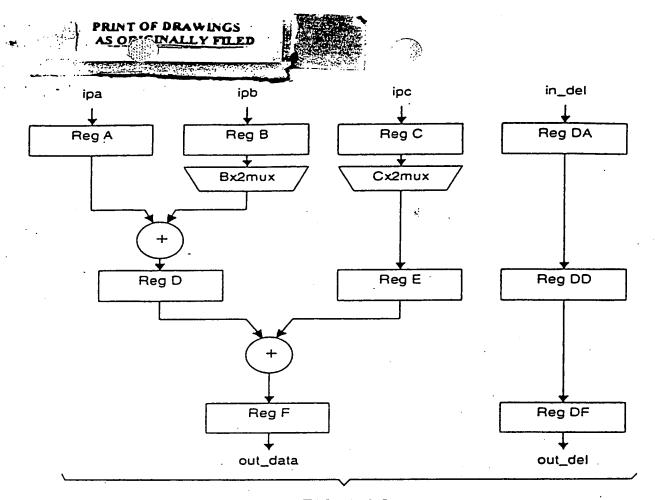


FIG. 148

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55
56	57	58	59	60	61	62	63

FIG. 149

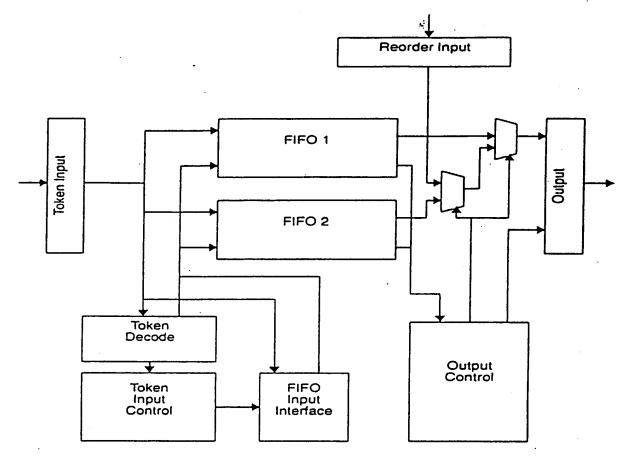


FIG. 150

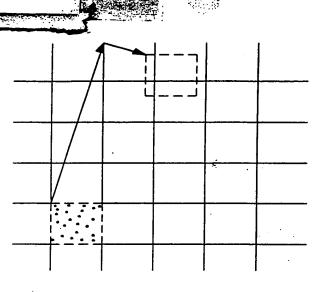


FIG. 151

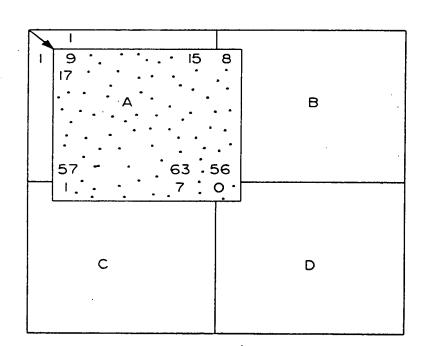
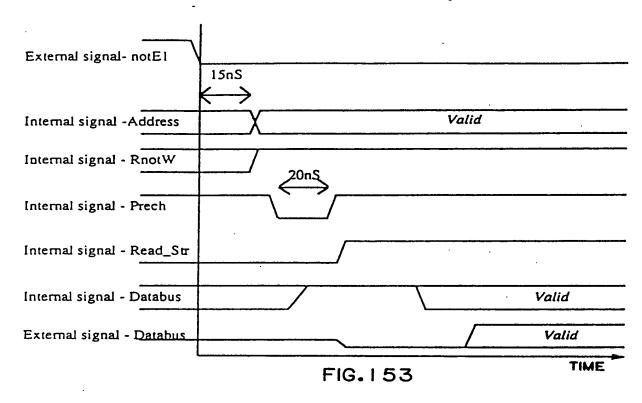
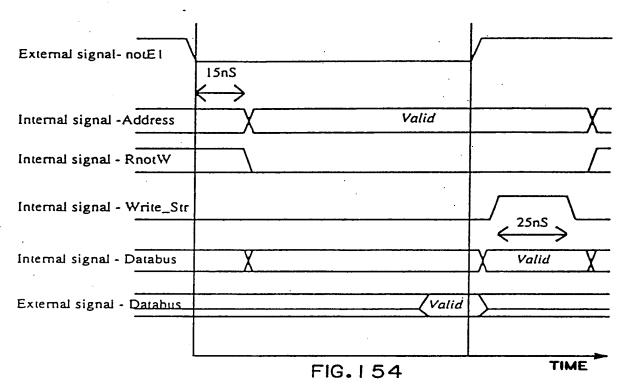


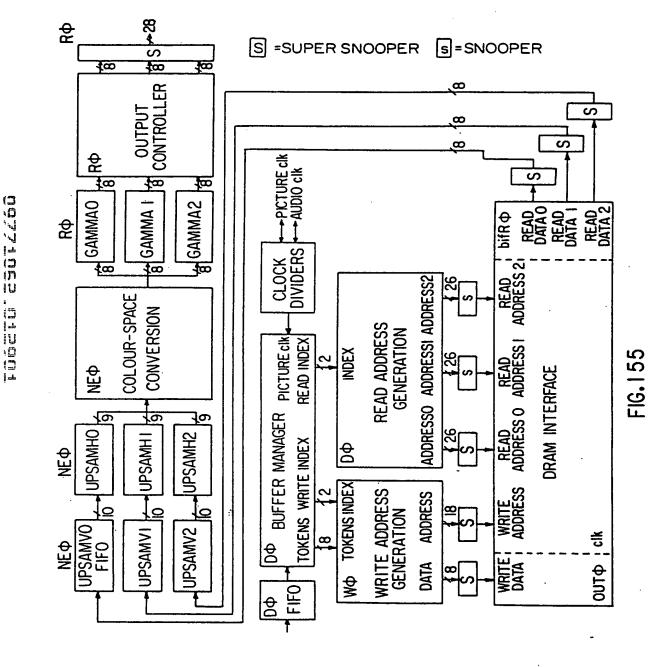
FIG. 152

Read Cycle



Write Cycle





.

- C. C. C.

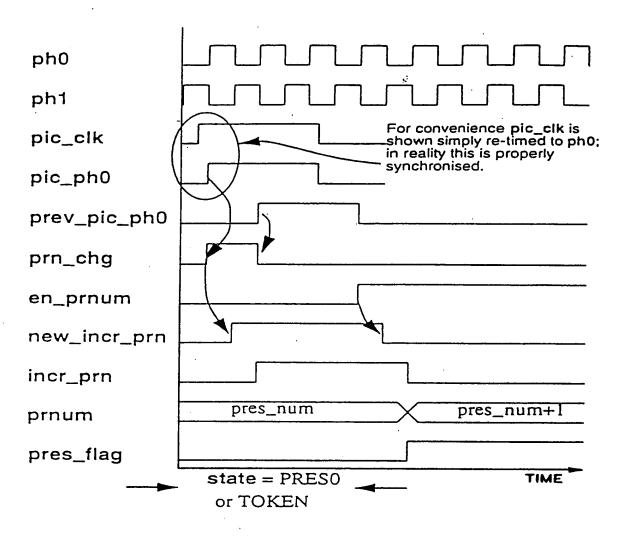


FIG. 156

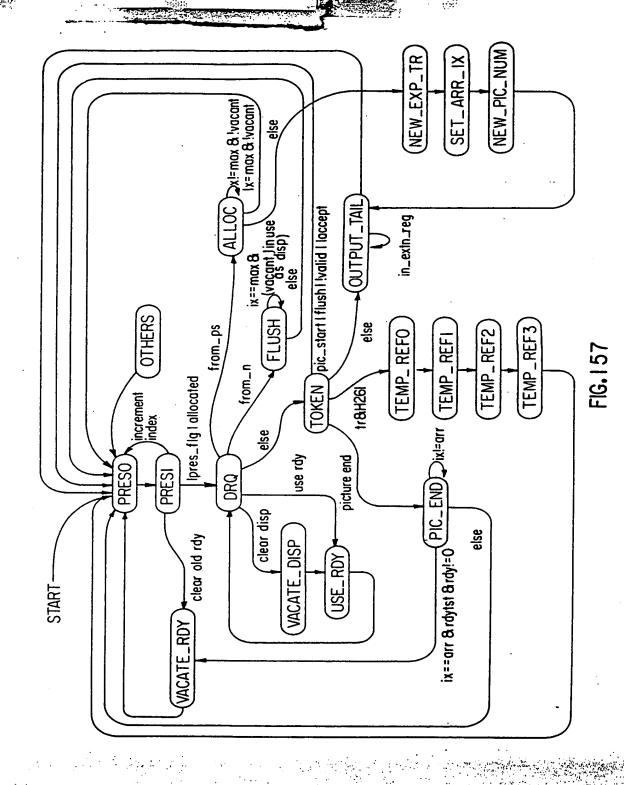
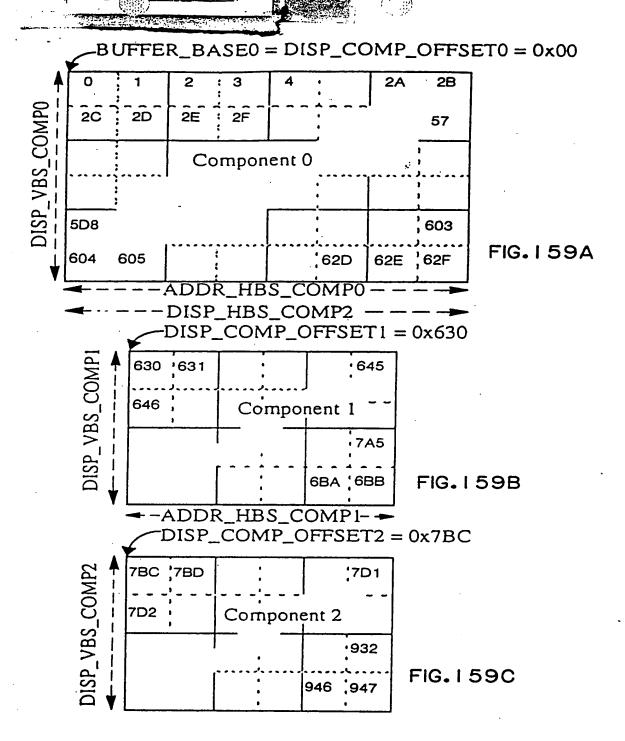
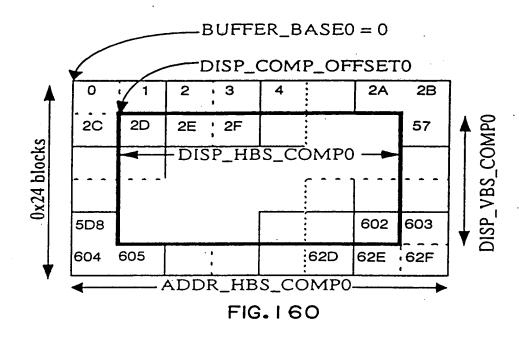


FIG. 158







BUFFER OFFSET 0x00

COMPONENT OFFSET 0x000 +

00	01	02	03	04	05	06	07	08	09	OA	OB
OC	OD	OE	OF	10	11	12	13	14	15	16	17
18	19	1A	1B	1 C	1D	1E	1F	20	21	22	23
24	25	26	27	28	29	2A	2B	2C	2D	2E	2F
30	31	32	33	34	35	36	37	38	39	3A	3B
3C	3D	3E	3F	40	41	42	43	44	45	46	47
48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
54	55	56	57	58	59	5A	5B	5C	5D	5E	5F
60	61	62	63	64	65	66	67	68	69	6A	6B
6C	6D	6E	6F	70	71	72	73	74	75	76	77
78	79	7A	7B	7C	7D	7E	7F	80	81	82	83
84	85	86	87	88	89	8A	88	8C	8D	8E	8F

FIG. 161A

COMPONENT1 OFFSET 0x100 +

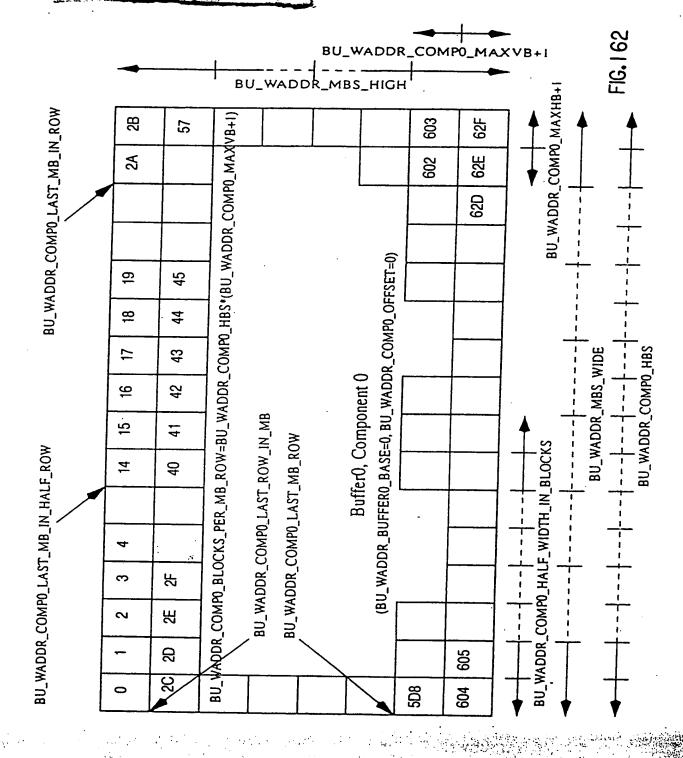
00					
06					
OC	OD	OE	OF	10	11
12	13	14	15	16	17
	19				
1E	1 F	20	21	22	23

- FIG. 16 1B

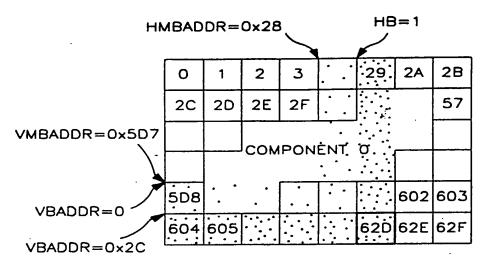
COMPONENT1 OFFSET 0x200 +

00					
06					
OC	OD	E	OF	10	11
12	13	14	15	16	17
		1A			
1E	1F	20	21	22	23

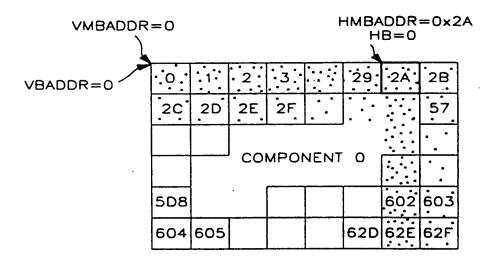
FIG. 1610



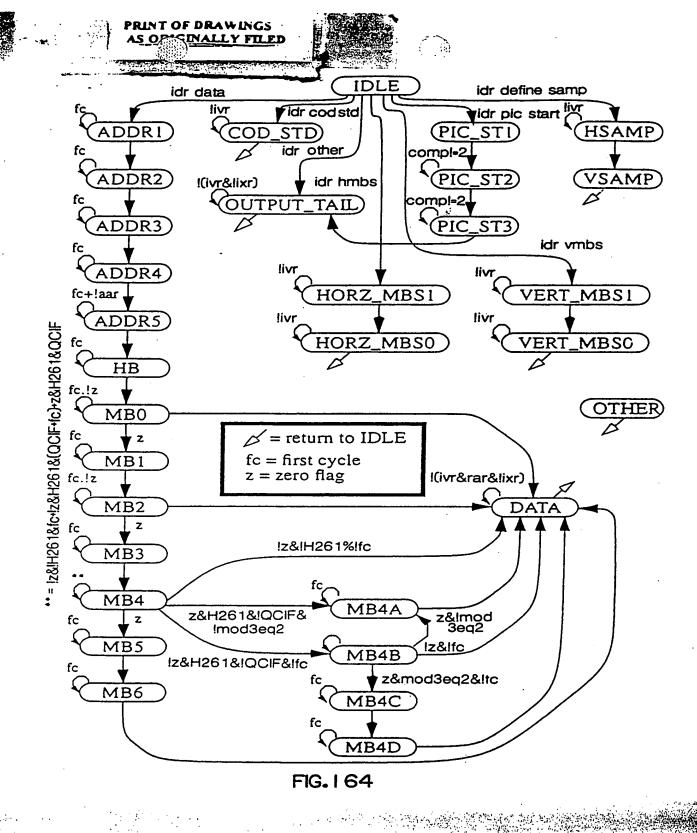
COPYTHE CIPCLE

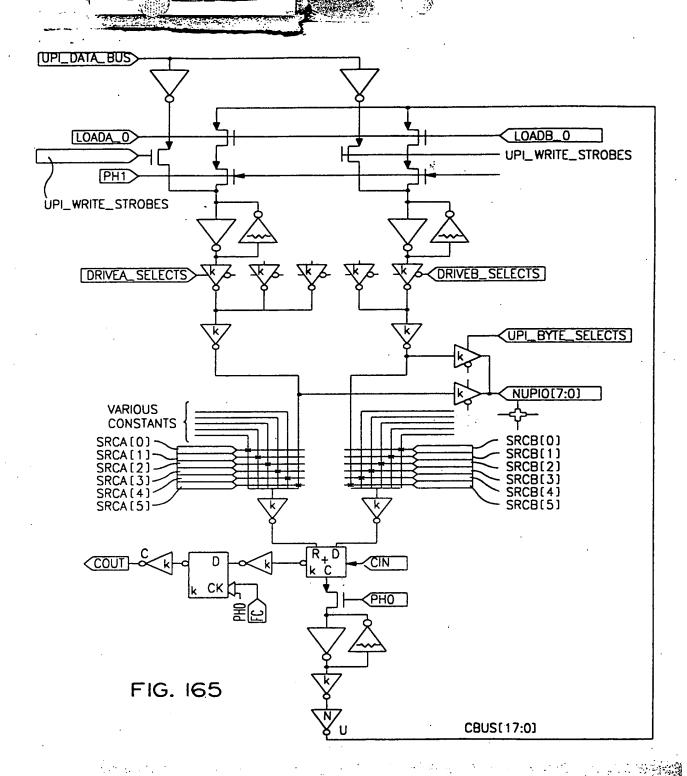


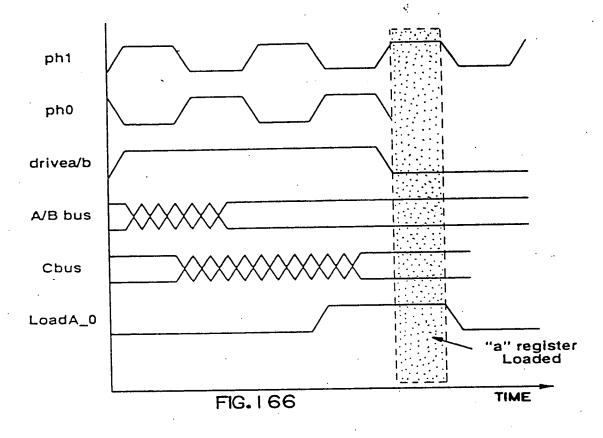
BLOCK ADDRESS=0+0+0x5D8+0x28+0x2C+1=0x62D FIG. I 63A



BLOCK ADDRESS=0+0+0+0x2A+0+0=0x2A FIG. I 63B







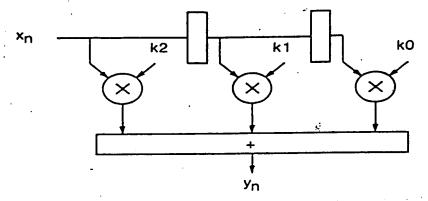
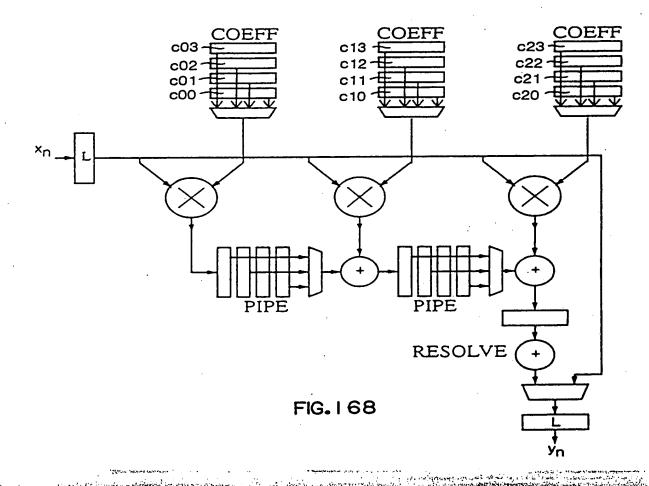


FIG. 167



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